

FIG. 1

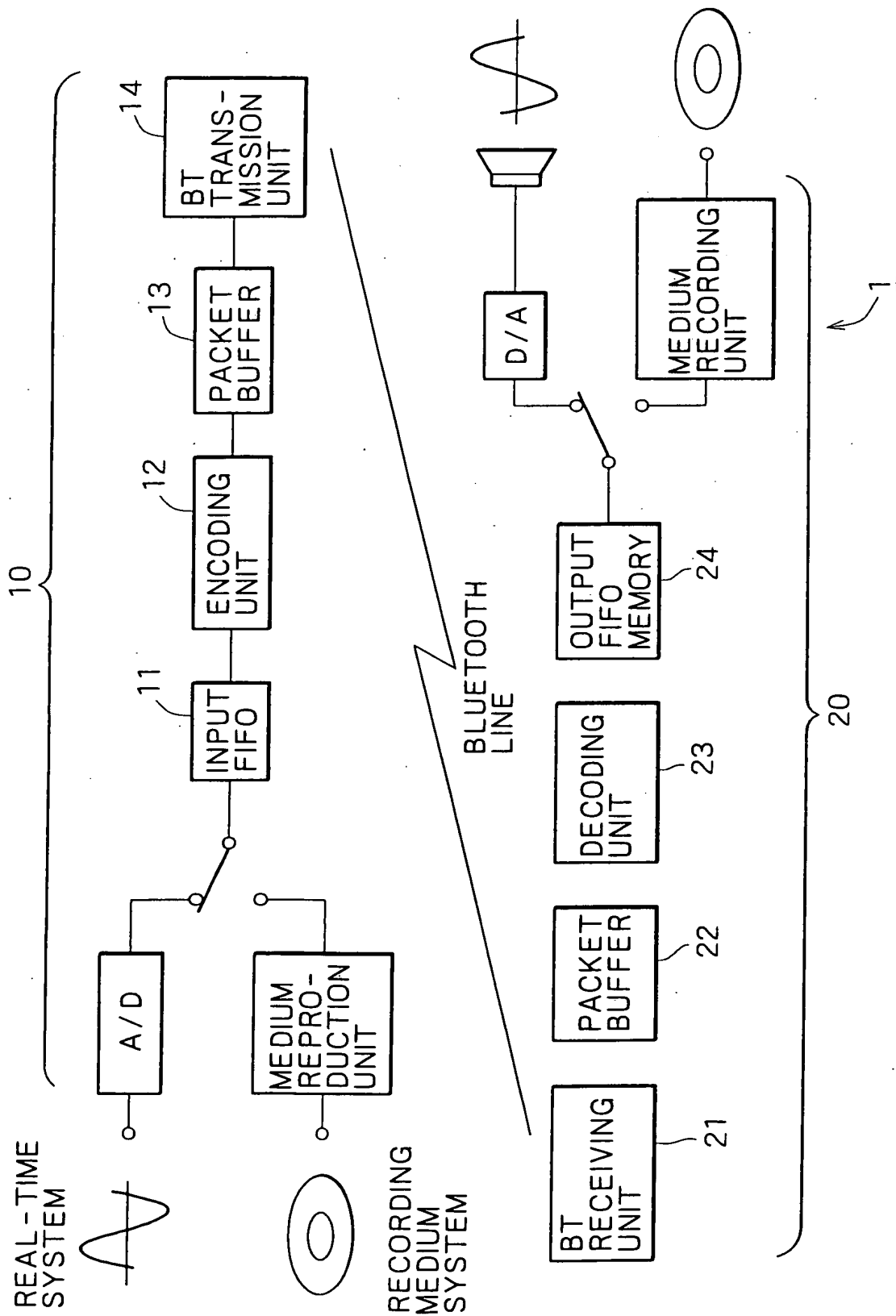


FIG. 2

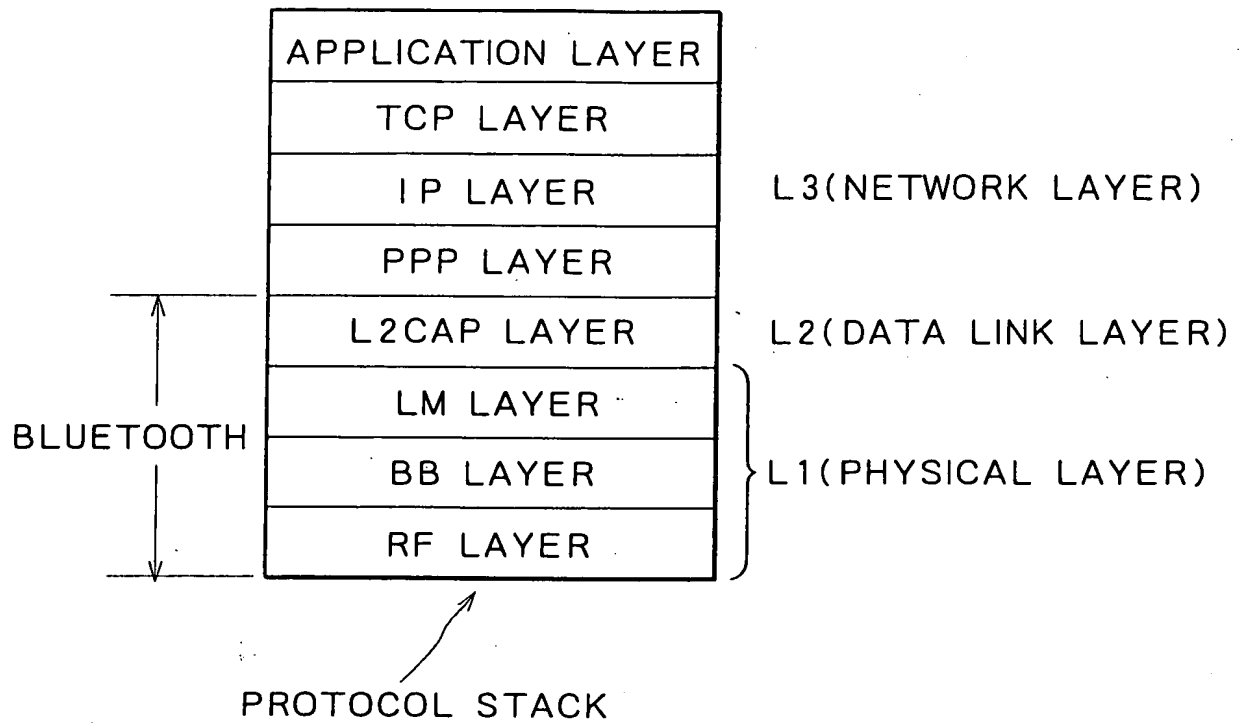


FIG. 3

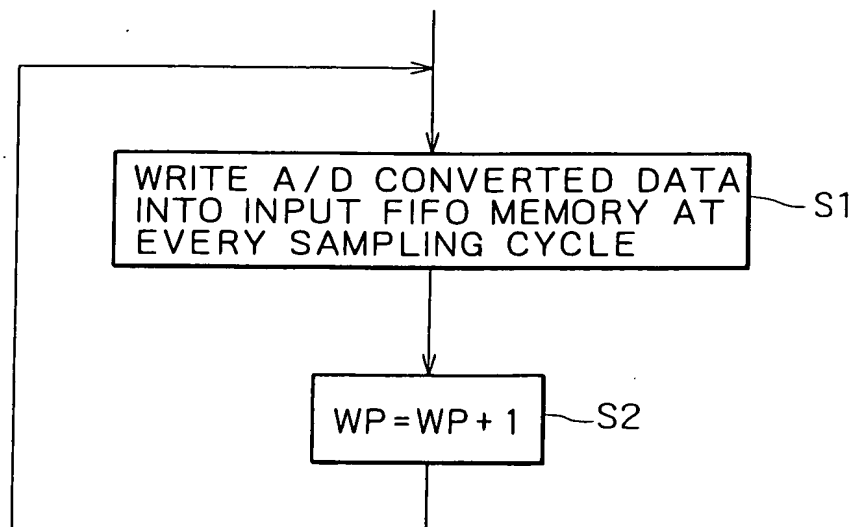
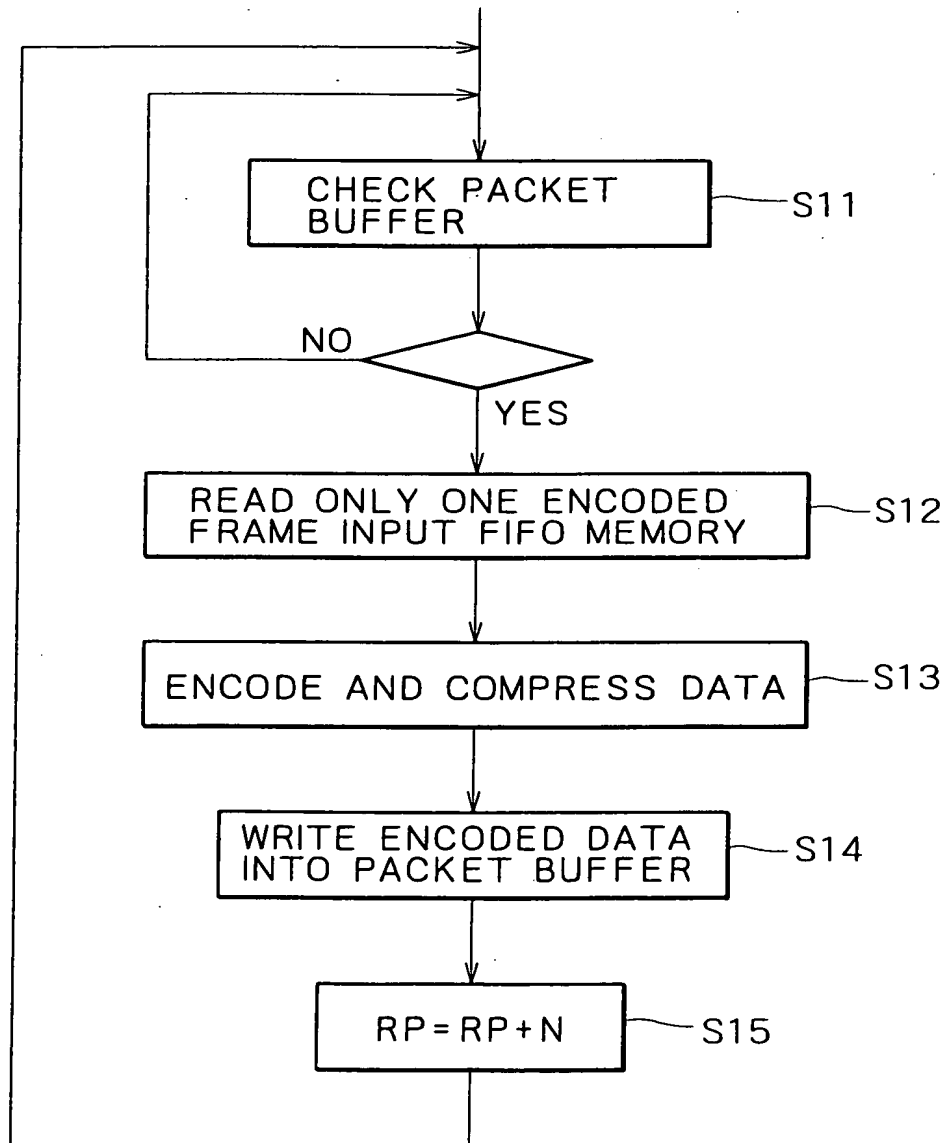


FIG. 4



09940551 082701

FIG. 5

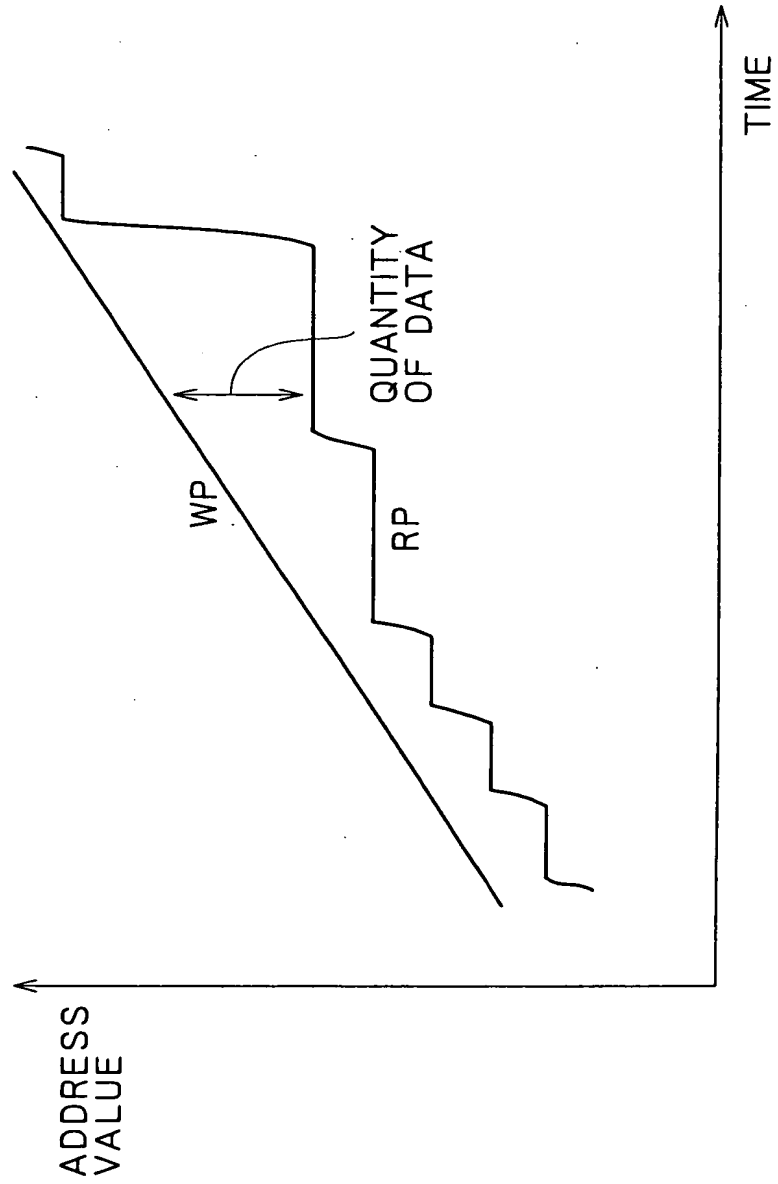
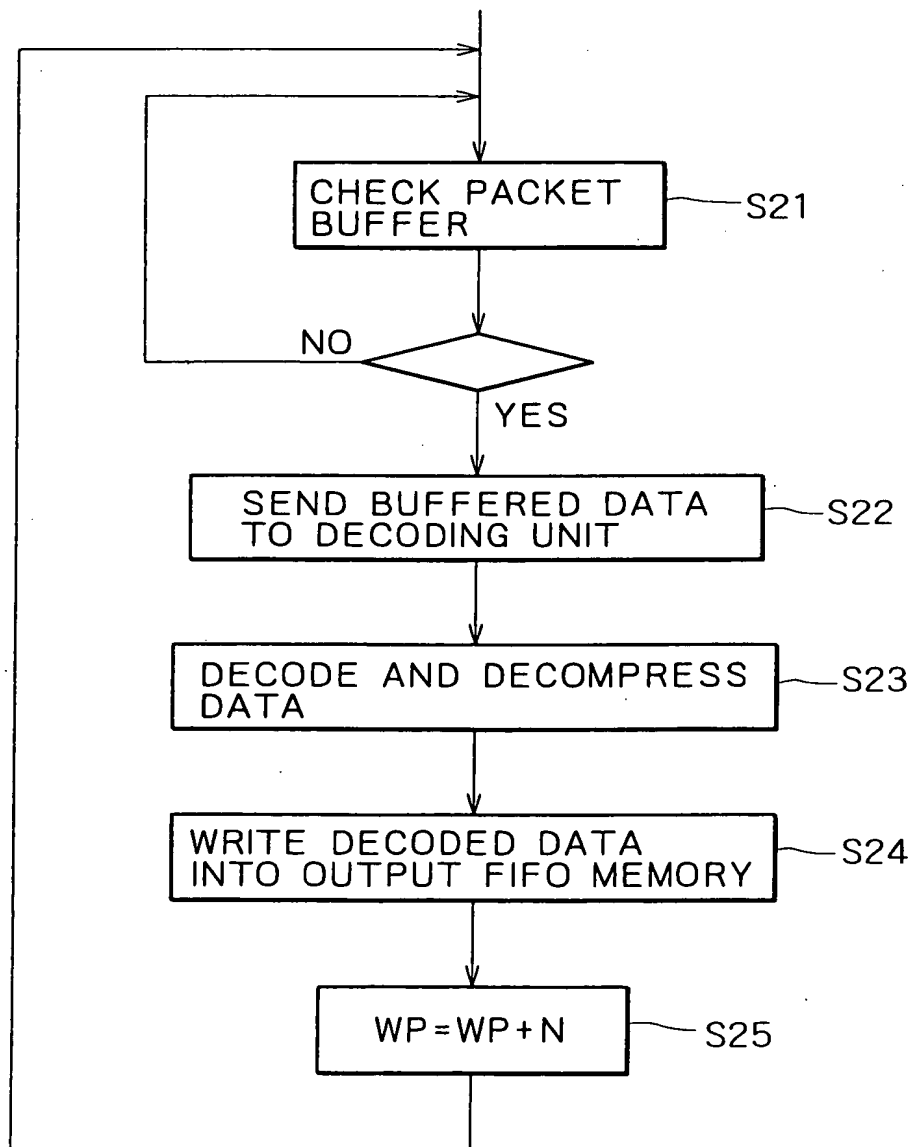


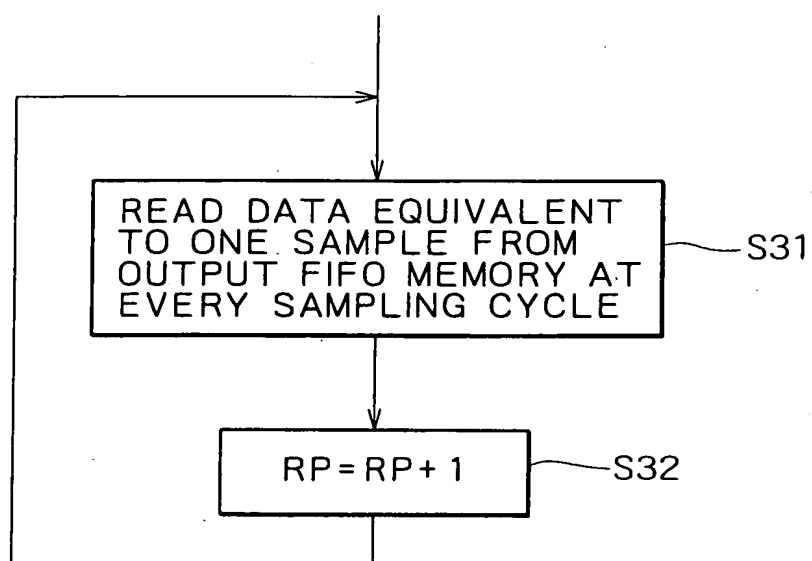
FIG. 6



09940551.082701

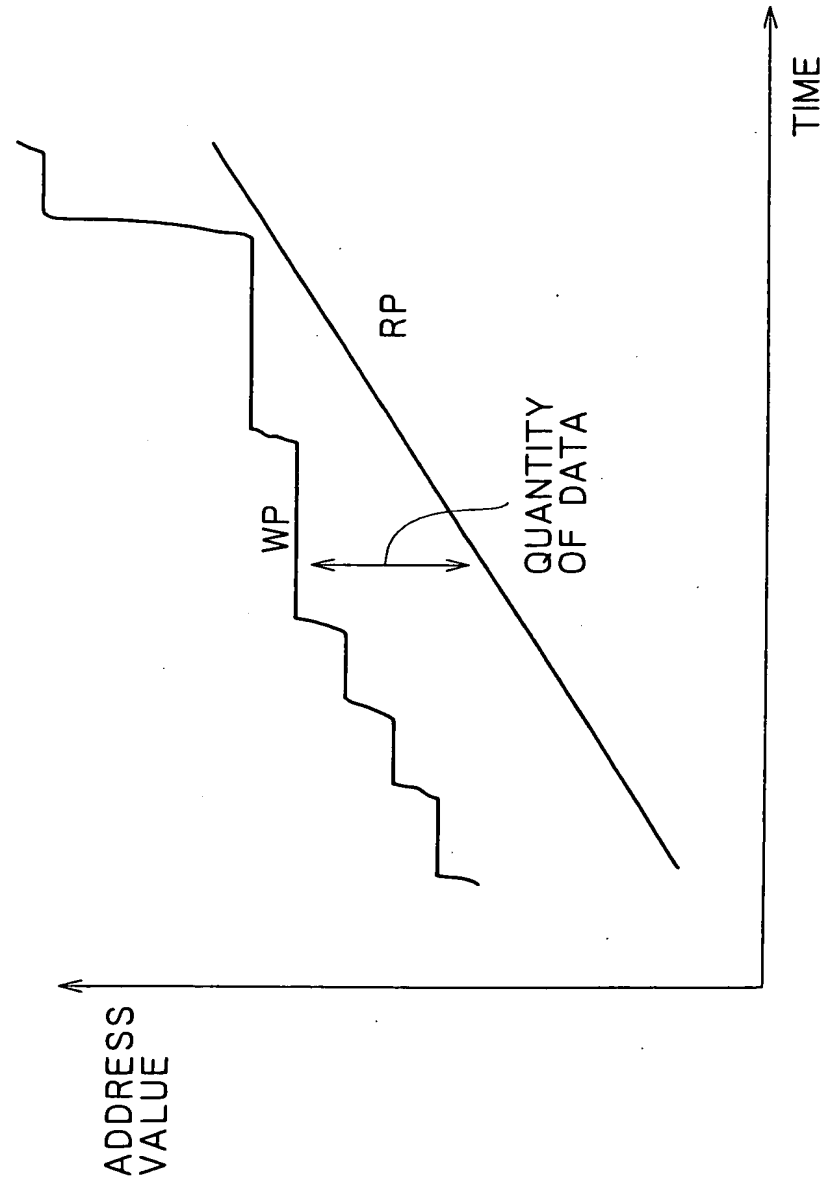
09940551-082701

FIG. 7

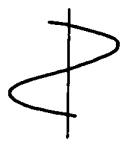


10/280" T5504650

FIG. 8



REAL-TIME
SYSTEM



A/D

MEDIUM
REPRO-
DUCTION
UNIT



RECORDING
MEDIUM
SYSTEM

FIG. 9

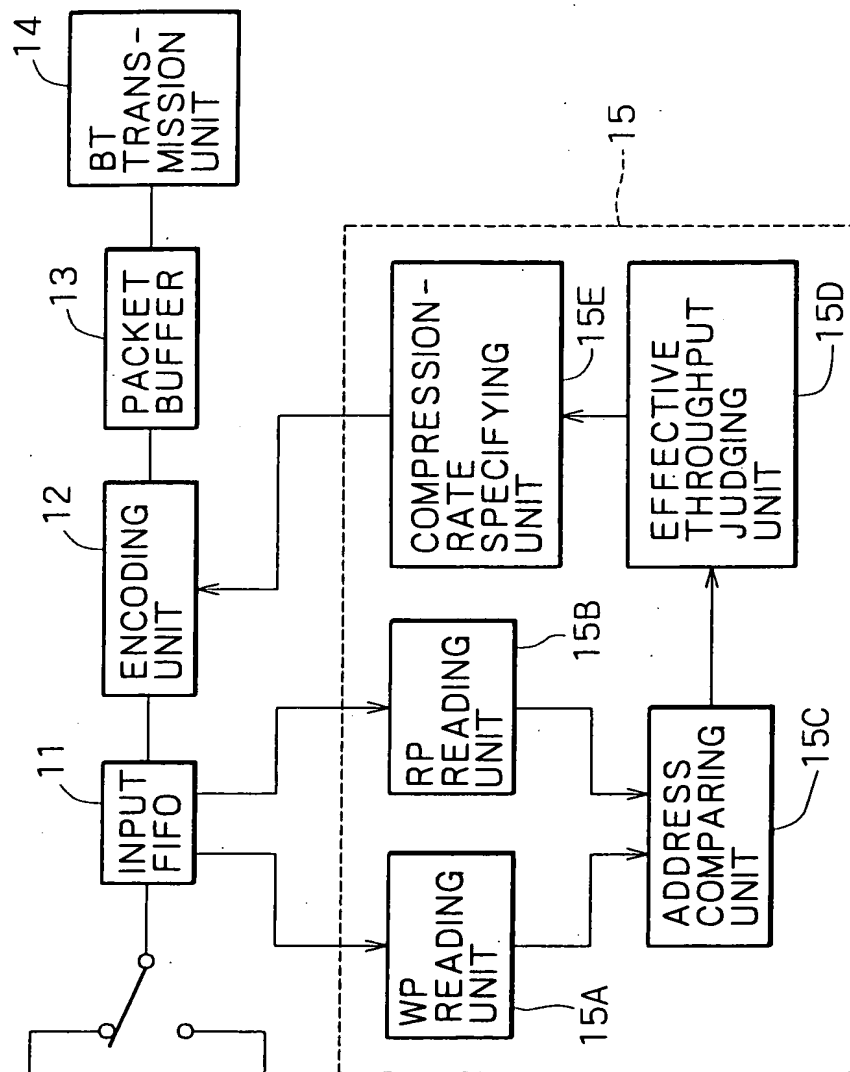
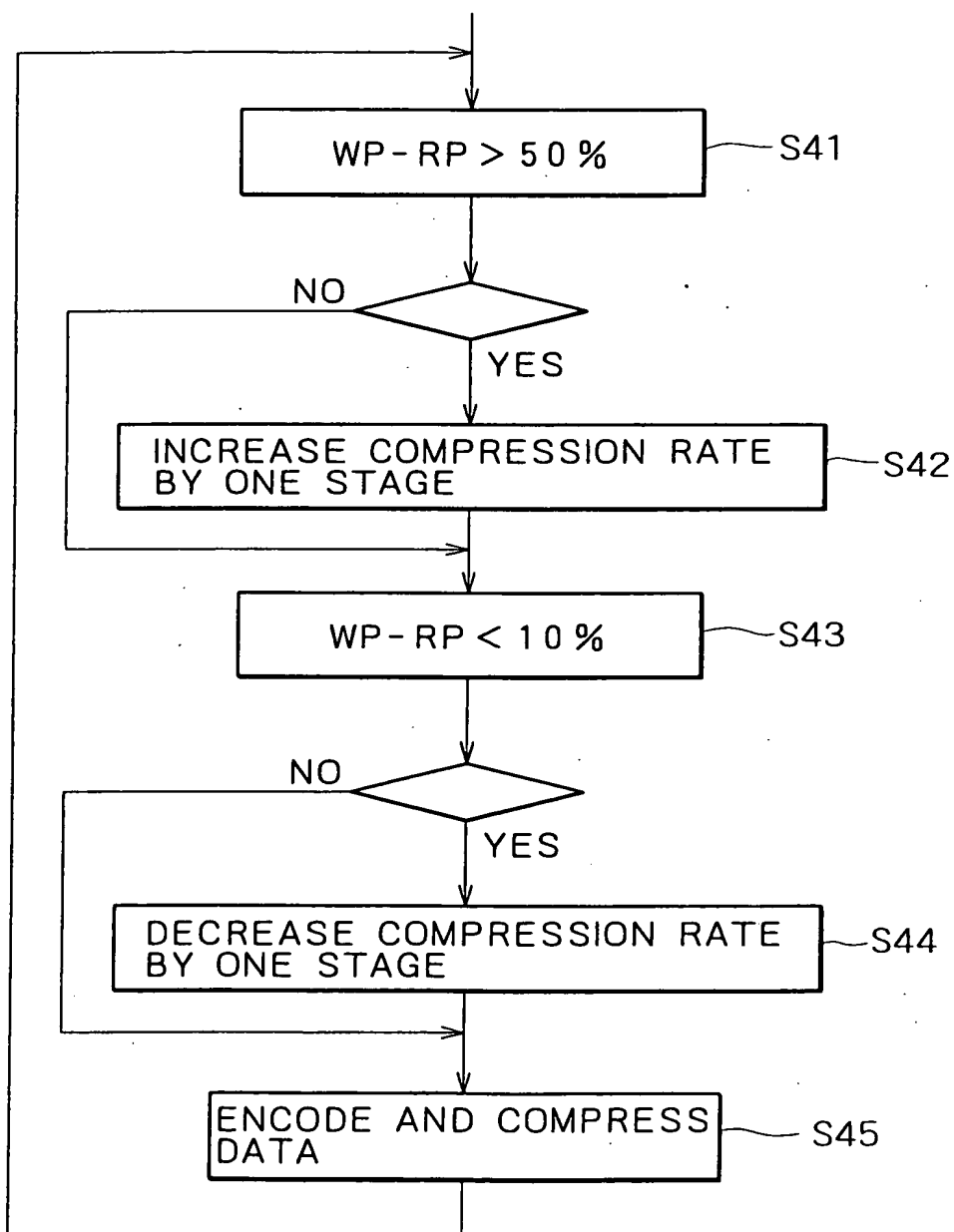
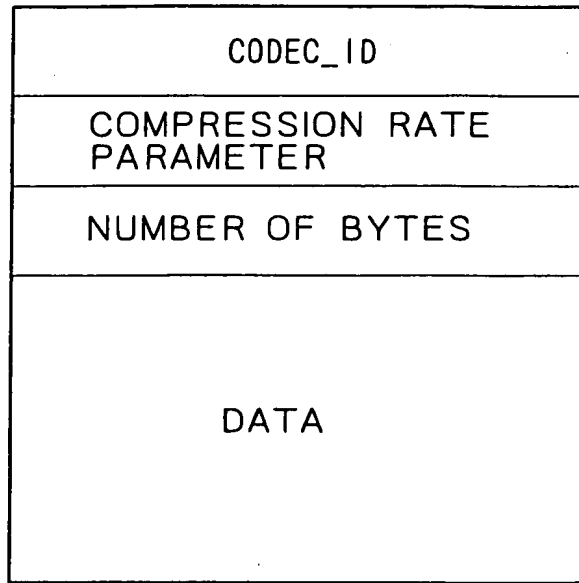


FIG. 10



0940551.082701
T0280 T550460

FIG. 11



DATA FRAME

FIG. 12

《 DATA CHANNEL 》

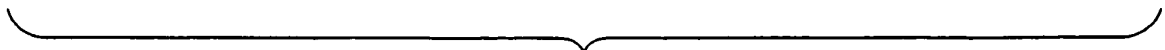
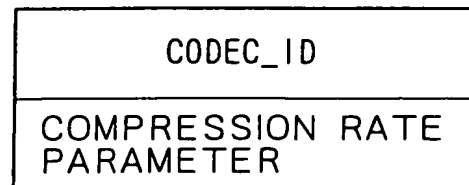
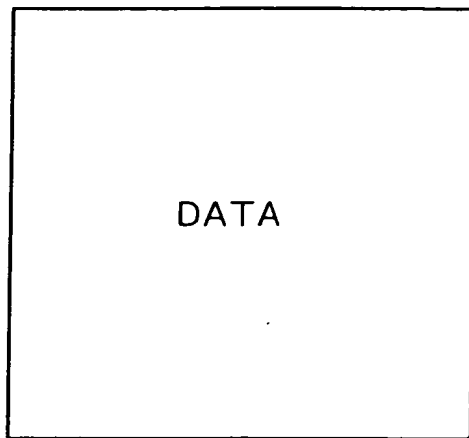


FIG. 13

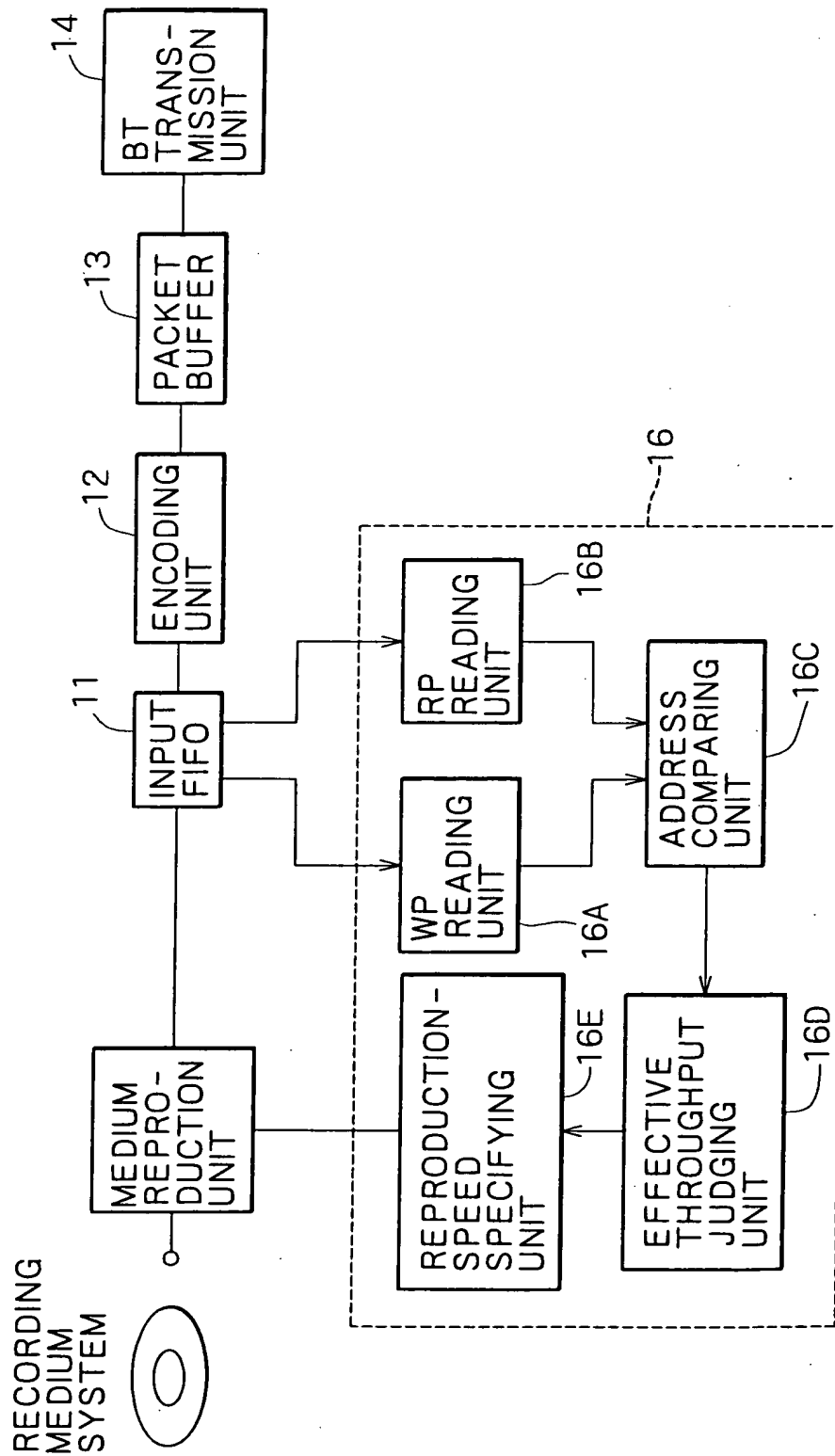
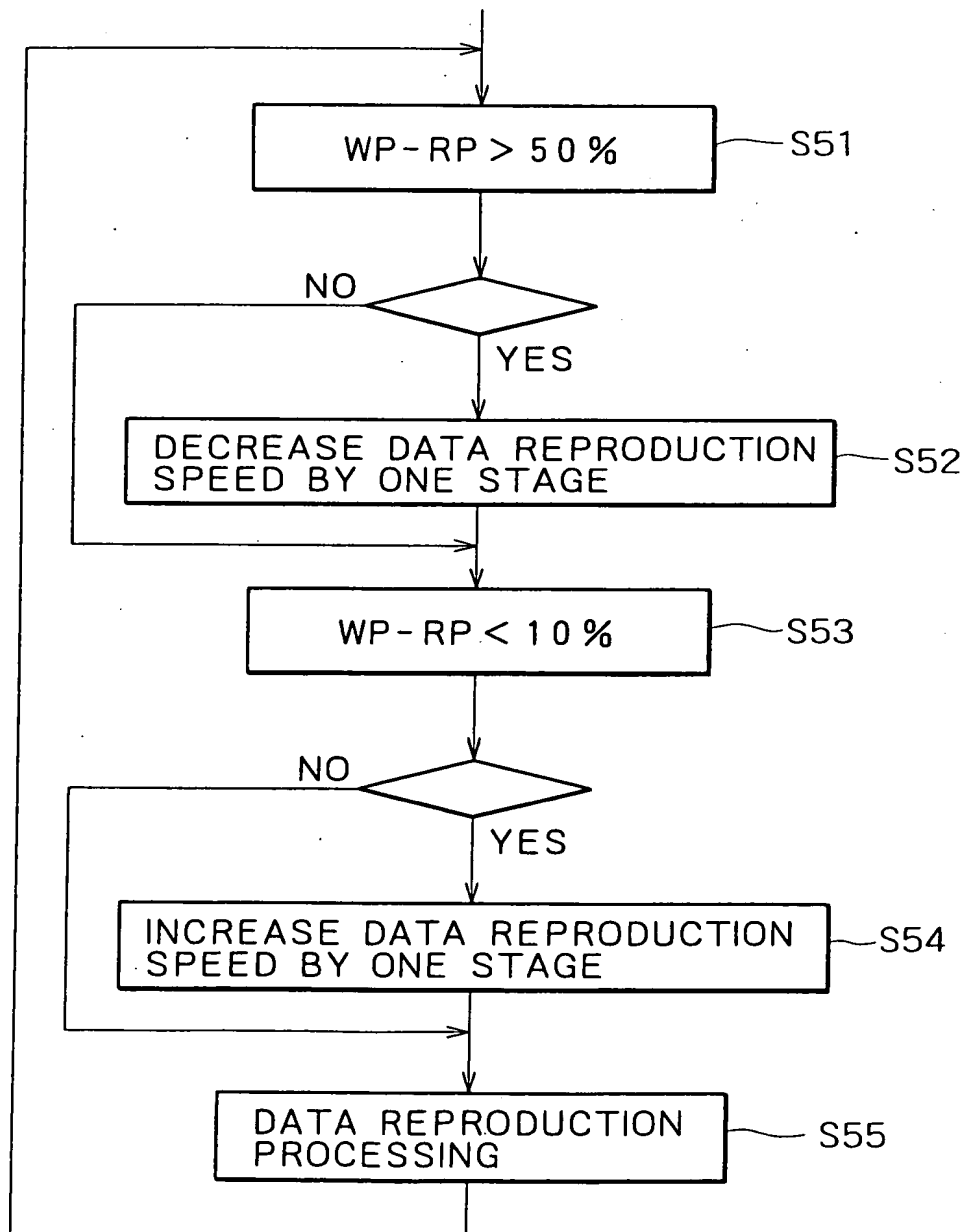


FIG. 14



09940551.082701

FIG. 15

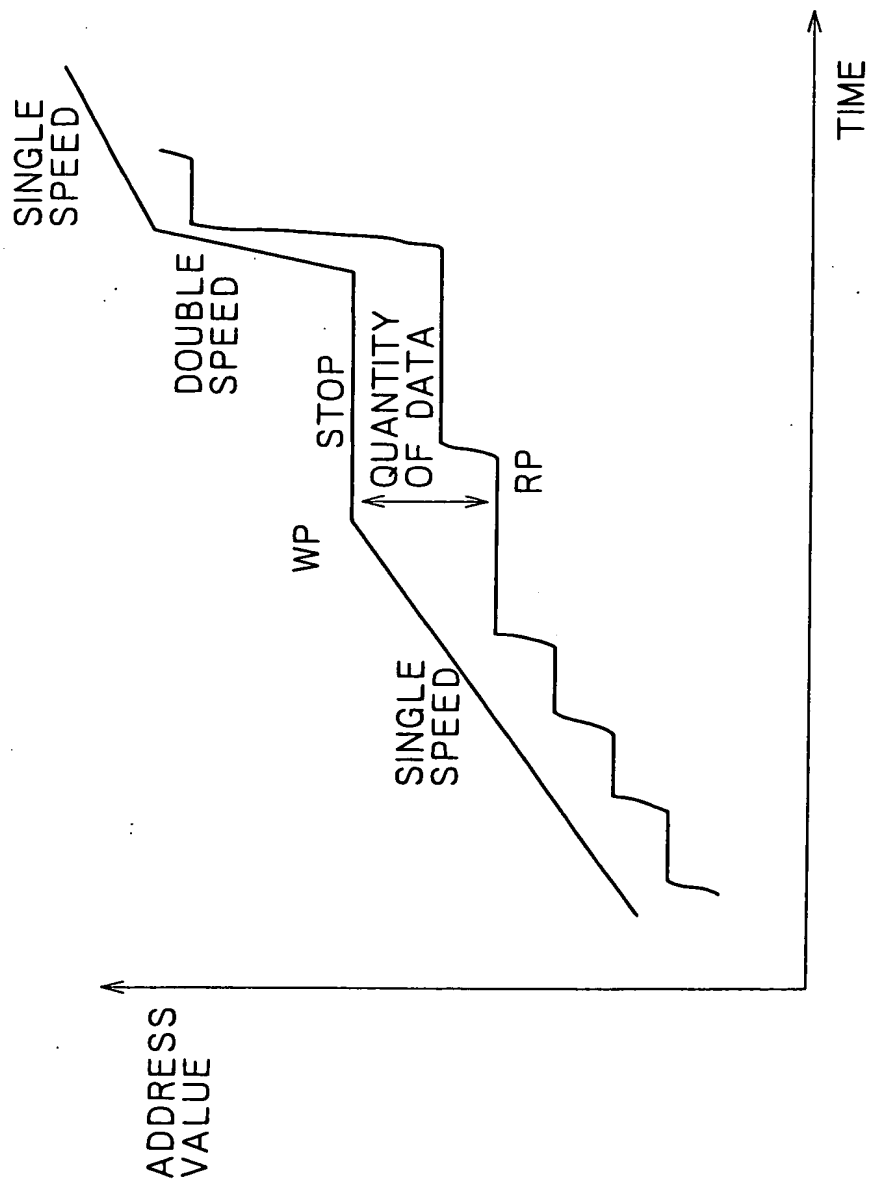


FIG. 16

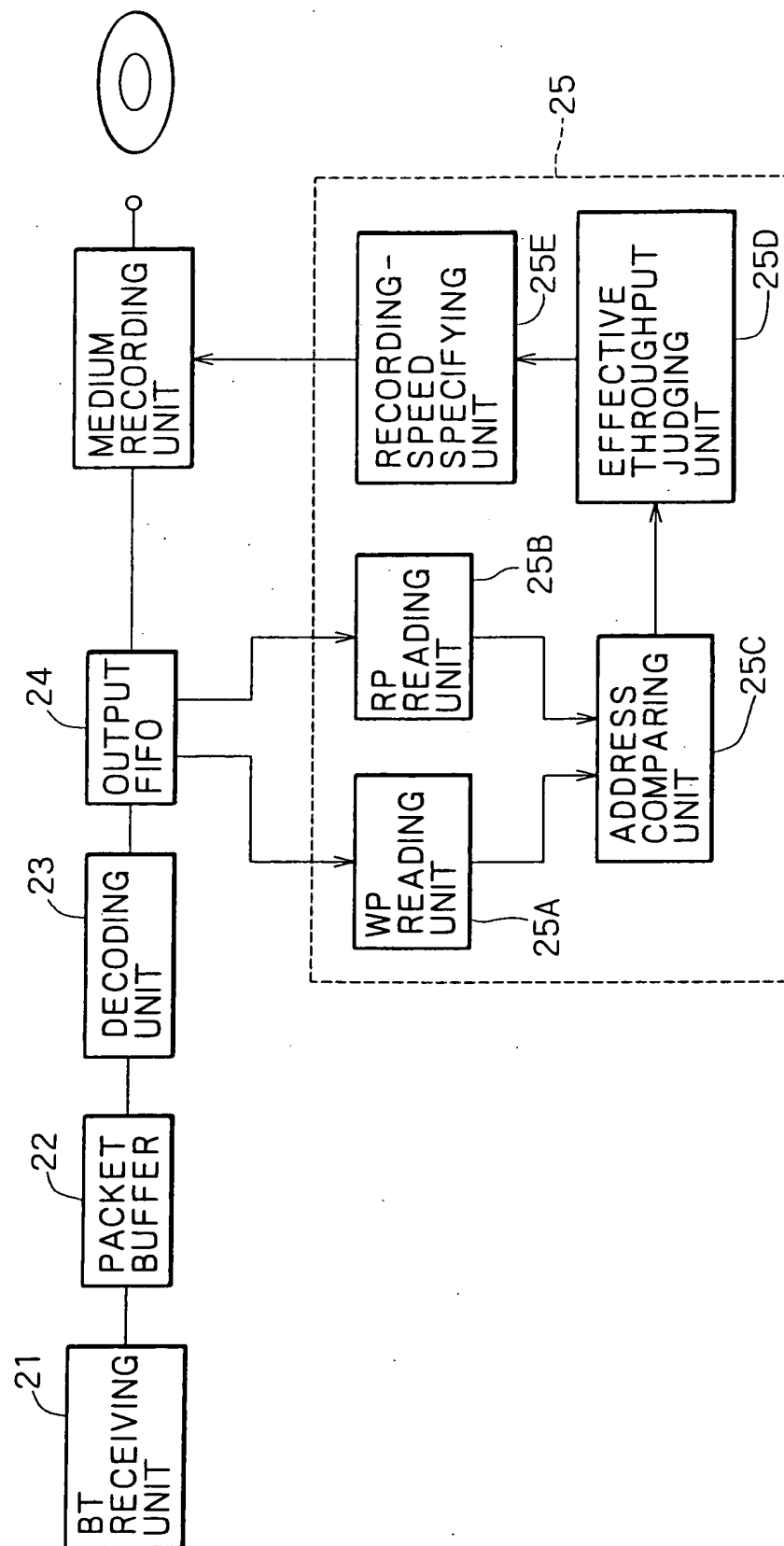
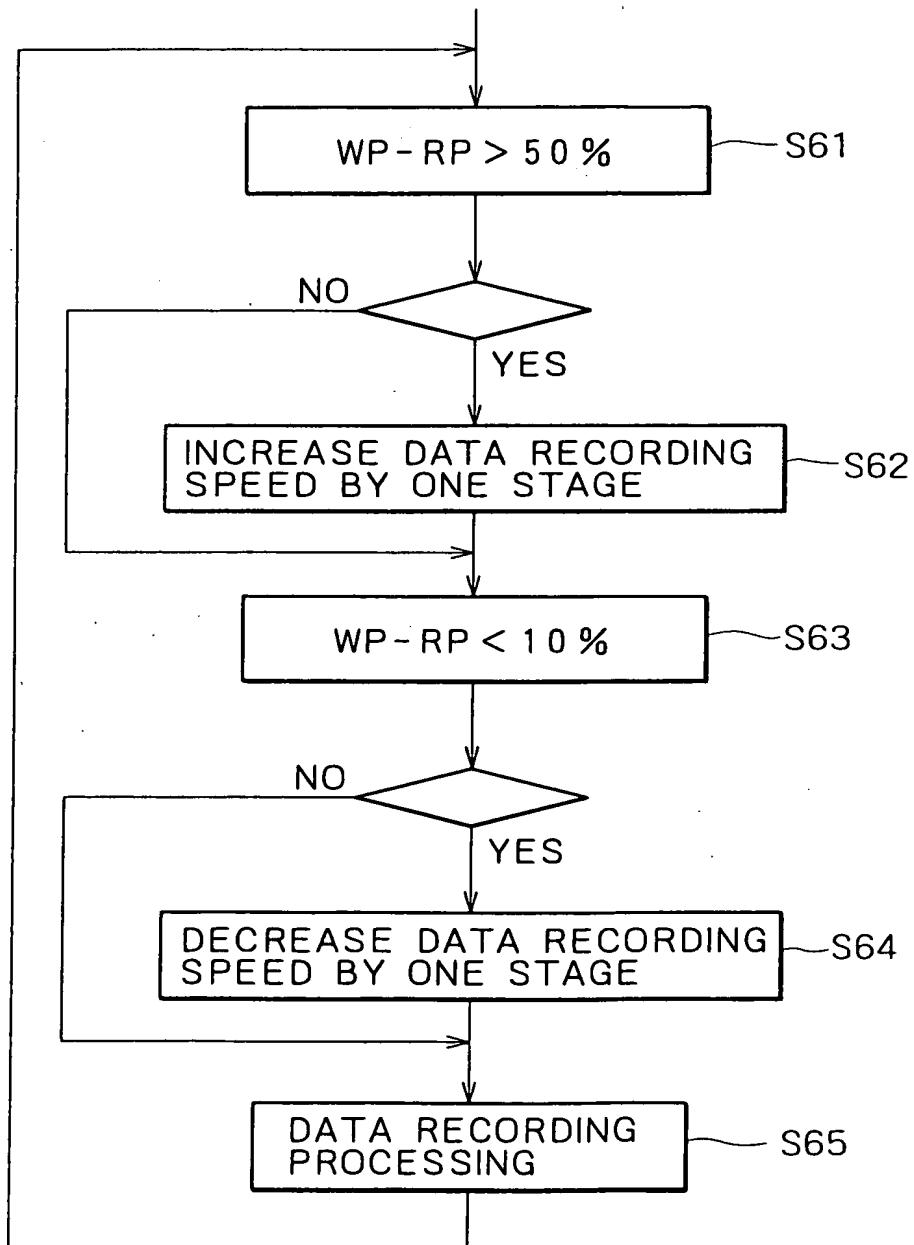


FIG. 17



0940551.0820

FIG. 18

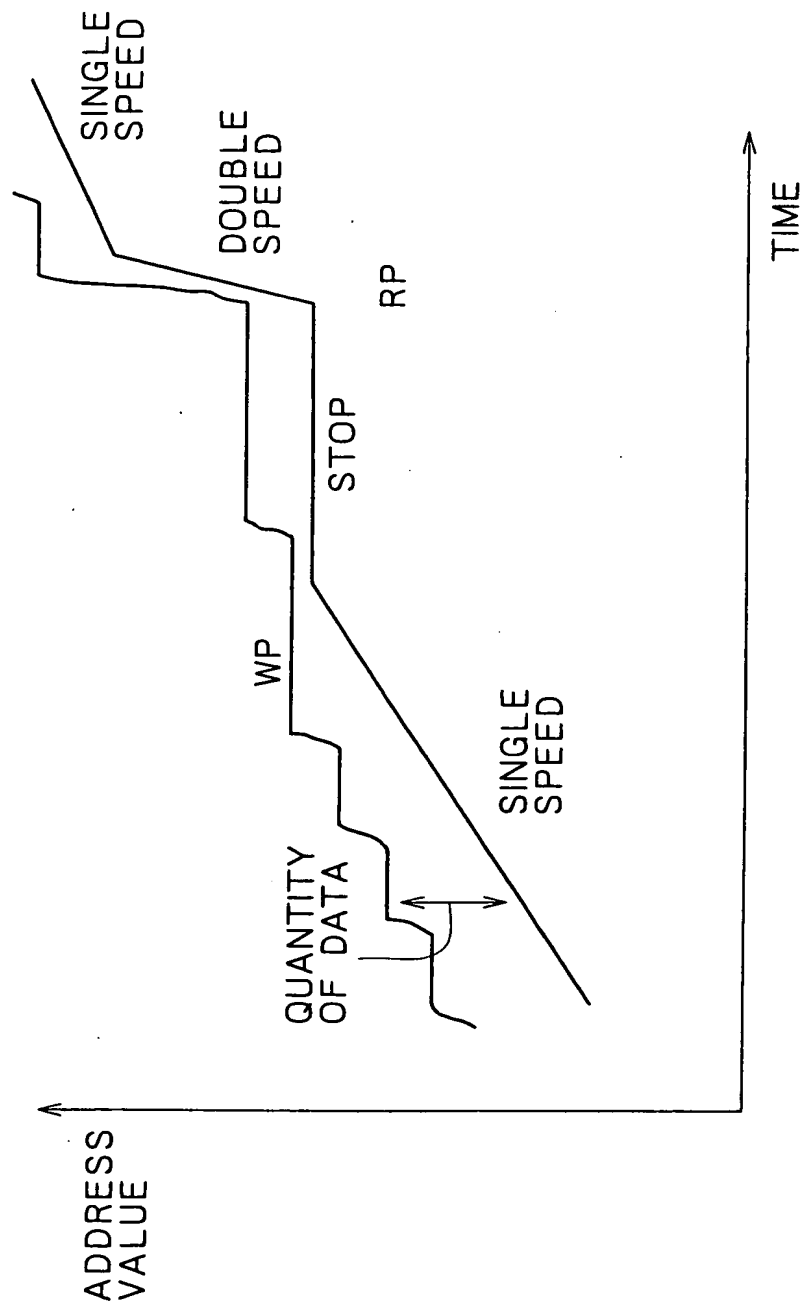


FIG. 19

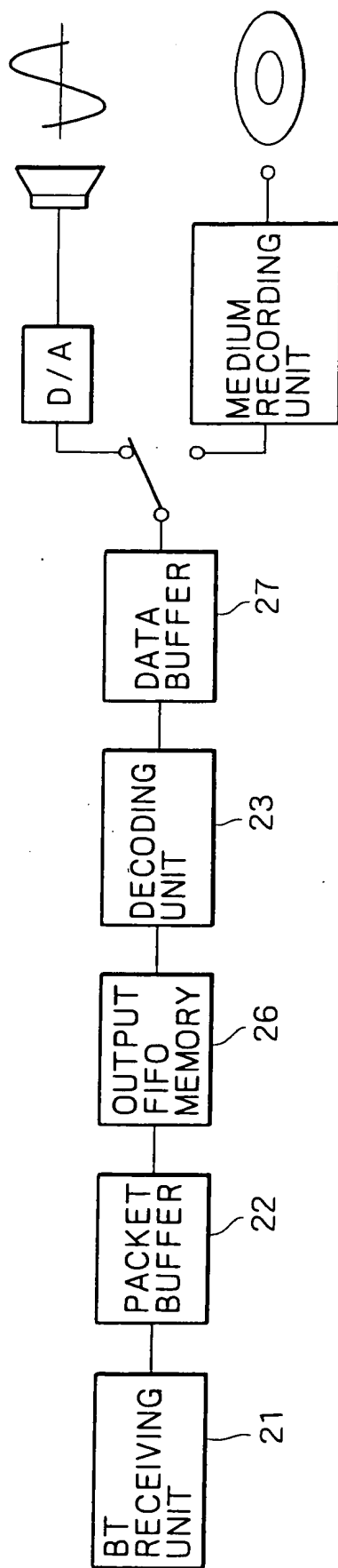


FIG. 20

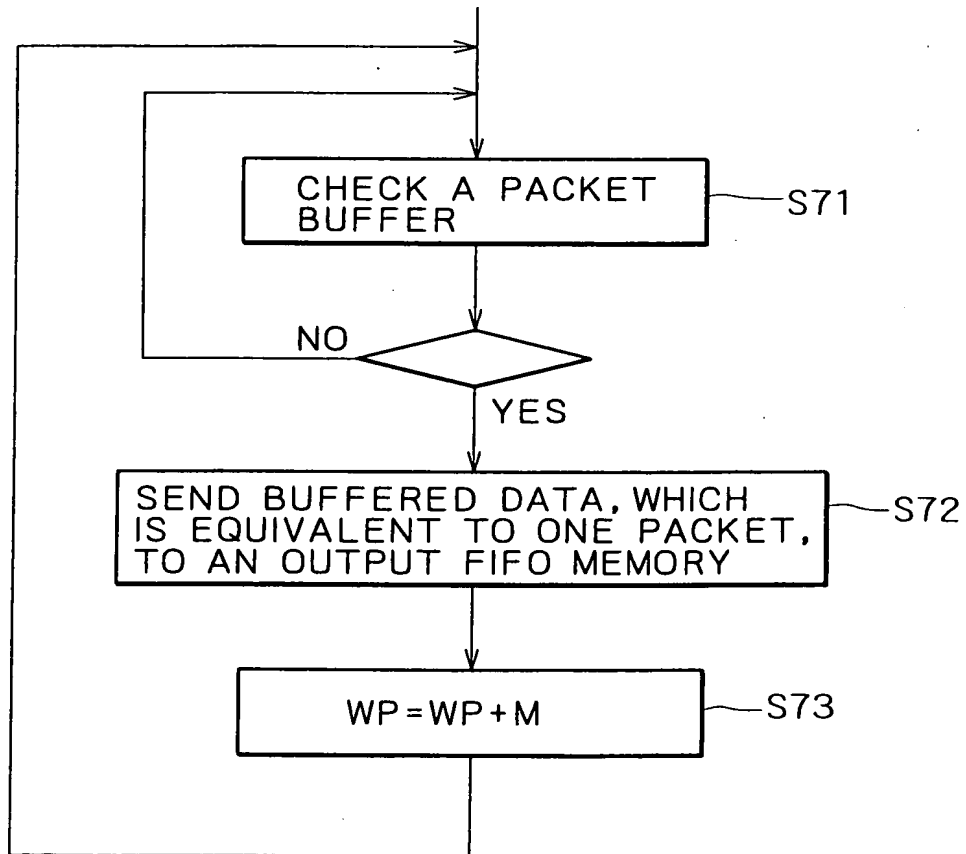
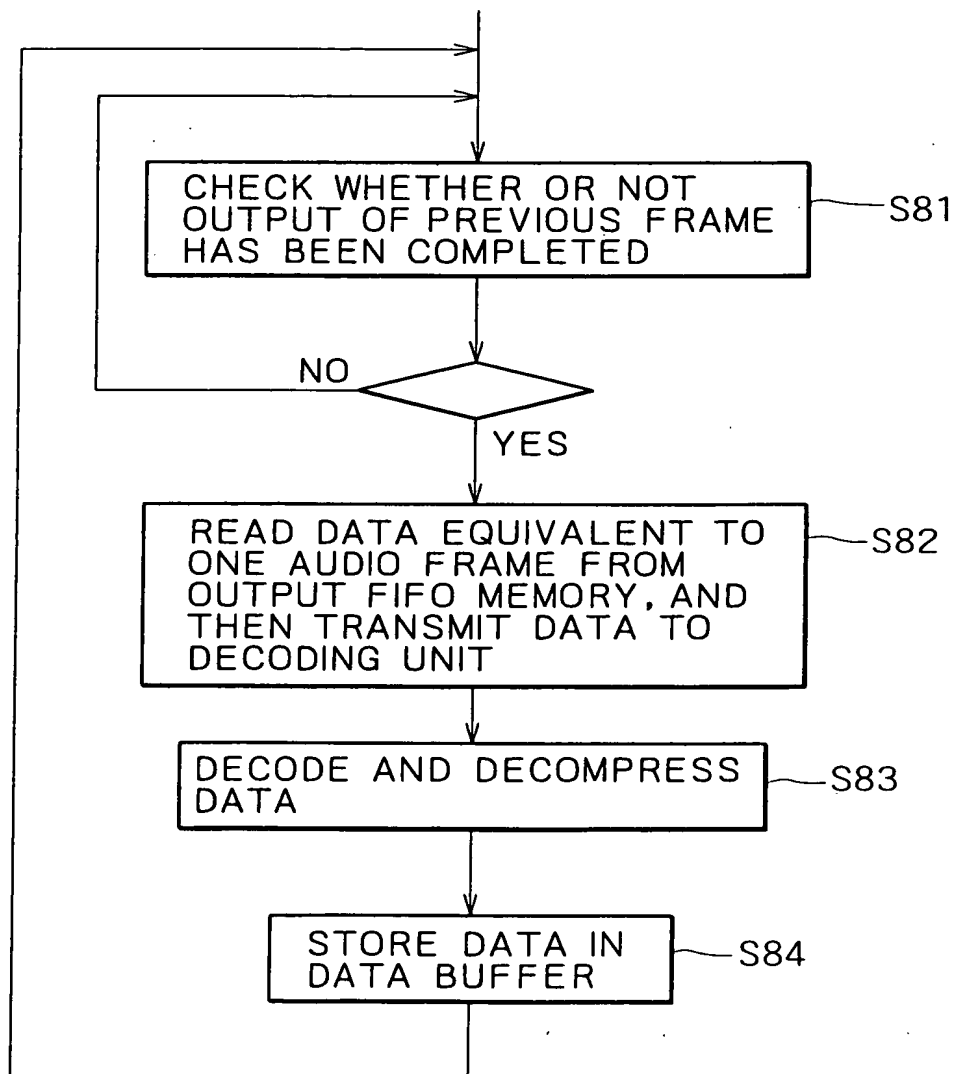


FIG. 21



TO 280" F5504660

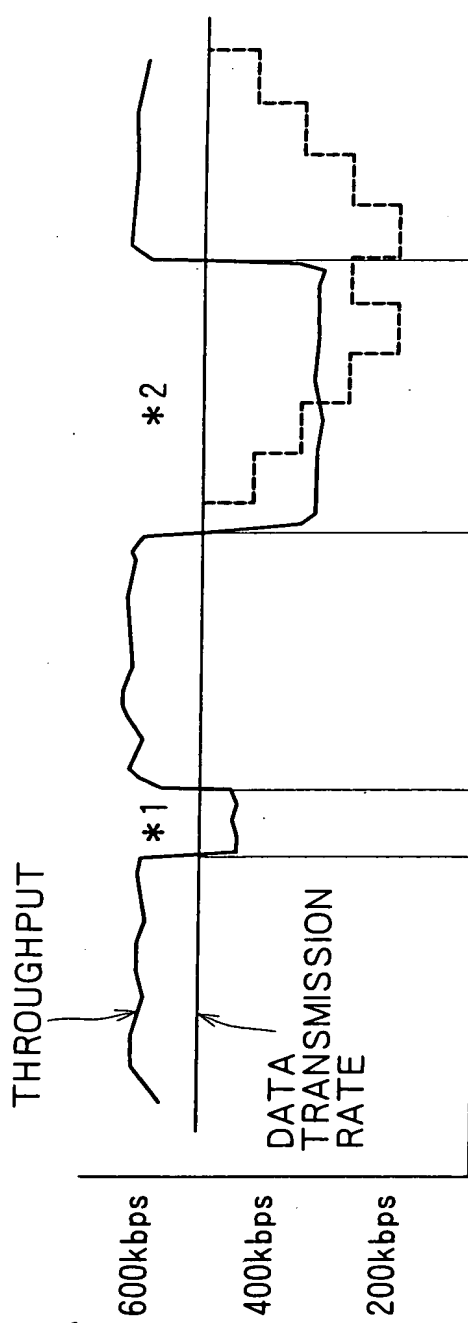


FIG. 22B

INPUT FIFO
MEMORY

FIG. 22C

OUTPUT FIFO
MEMORY

FIG. 22D

